



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Confirmation No.: 2380

Satoru Miyagi

Art Unit: 2816

Serial No.: 10/674,016

Examiner: Kenneth B. Wells

Filed: September 30, 2003

Docket No.: 100021-00133

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING A  
LEAKAGE CURRENT CUTOFF CIRCUIT, CONSTRUCTED USING  
MT-CMOS, FOR REDUCING STANDBY LEAKAGE CURRENT

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450


Date: 3/29/05

Sir:

Applicant(s) petition for an extension of time for one month for response to the final Office Action dated December 01, 2004 from March 1, 2005 to April 1, 2005.

Please find attached a check in the amount of \$120.00 to cover the fee discussed above. The Commissioner is hereby authorized to charge any fee deficiency or credit any overpayment associated with this communication to Deposit Account No. 01-2300.

Respectfully submitted,

  
Michele L. Connell  
Registration No. 52,763

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02 FC:1251 120.00 DP

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